

# 3 WAYS TO CONNECT What You Need

Phase you're in:

You'll need:

## Research & Development

- De-bug
- Good for R&D or lab environment
- "Fast and dirty"

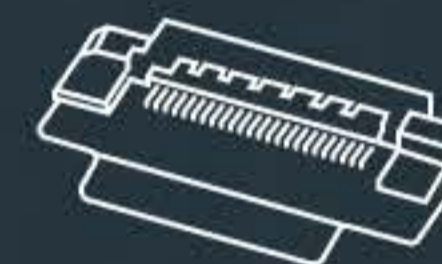


LOADSLAMMER



PTO  
PROTECTOR +  
POWER TAKE OFF

or



PROTECTOR

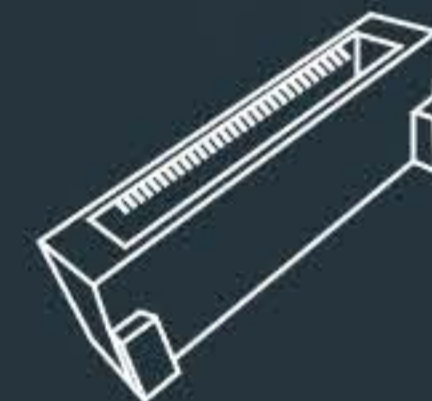
- ✓ No PCB modifications required
- ✓ Fastest to implement
- ✗ Placement maybe non-optimal, typically constrained by capacitor location
- ✗ Non-optimal impedance

## Validation & Characterization

- Complex + FPGA
- Comprehensive testing
- Prototyping + pilot runs



LOADSLAMMER



CONNECTOR

or



PASSIVE ADAPTOR

- ✓ Faster to implement
- ✓ More repeatable results
- ✗ Slower test time
- ✗ No thermal emulation

## Multi-site Testing

- Enterprise class repeatability
- Highest slew rate di/dt
- Ecosystem friendly



CONTROLLER  
(BRAIN) ORAC-JR



ACTIVE  
ADAPTOR

- ✓ No PCB modifications required
- ✓ Fastest testing
- ✓ Most accurate
- ✓ Technician friendly
- ✓ Thermal emulation
- ✗ Requires custom adaptor development